

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination SHIMIZU ET AL.	
		Examiner	Art Unit	Page 1 of 1
<b>U.S. PATENT DOCUMENTS</b>				

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,469,832	10-2002	Yotsuya et al.	359/619
*	B	US-5,876,267	03-1999	Kanda, Shinji	451/38
*	C	US-2003/0007772	01-2003	Borrelli et al.	385/137
*	D	US-4,012,263	03-1977	Shell, James Allen	501/70
*	E	US-6,407,866	06-2002	Yamashita et al.	359/619
*	F	US-6,618,200	09-2003	Shimizu et al.	359/619
*	G	US-5,593,528	01-1997	Dings et al.	156/154
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	✓ Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, pp. 532-33.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.